MXa SIL Guidance and Certification

SIL 3 capable for critical applications
Functional Safety in Plants

Safety and instrumentation engineers demand that a functional safety system’s probability of dangerous failures be greatly reduced in order to minimize the risk to humans and the environment. Functional safety can be defined as a safety function which insures that, when a device failure occurs, the device performs in a manner so as not to jeopardize plant safety. This means that the device performs its intended function when called upon (Emergency Shut Down or ESD mode) or its lack of performance (stay-put mode) does not increase the risk of further failure. A method for determining exposure levels of functional safety is defined in IEC 61508, Functional Safety of Electrical/Electronic/Programmable Electronic Safety-related Systems. It is complemented by IEC 61511, Functional safety — Safety Instrumented Systems for the Process Industry Sector. These two standards are used to aid engineers in designing systems that are functionally safe.

Safety Integrity Levels

Functional safety is vital in applications with potential to expose people and expensive equipment to random failures and their ramifications. This is especially true for equipment that employs microprocessors and programmable logic. Users want not only assurances, but also hard proof that confirms the equipment purchased for critical safety installations is safe, meeting the stipulations of IEC 61508. Plant operational functional safety categories are referred to as “SIL”, or Safety Integrity Level, a measurement of risk reduction beginning with level 1 and ascending to level 4. Each category change, level 1 to level 2 for example, reduces the risk by a function of 10, as seen below:

<table>
<thead>
<tr>
<th>SIL</th>
<th>PFDavg</th>
<th>RRF (Risk Reduction Factor)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>$10^0 \ldots &lt; 10^1$</td>
<td>10 000 to 100 000</td>
</tr>
<tr>
<td>3</td>
<td>$10^{-1} \ldots &lt; 10^{-2}$</td>
<td>100 to 1000</td>
</tr>
<tr>
<td>2</td>
<td>$10^{-2} \ldots &lt; 10^{-3}$</td>
<td>10 to 100</td>
</tr>
<tr>
<td>1</td>
<td>$10^{-3} \ldots &lt; 10^{-4}$</td>
<td>1 to 10</td>
</tr>
</tbody>
</table>

Safety Instrumented Systems and Probability of Failure on Demand

Each device installed into a Safety Instrumented System (SIS) should be evaluated independently to determine its FMEDA (Failure Modes Effects and Diagnostic Analysis) and subsequent safety tolerance values. Electric actuators do not, by themselves, comprise an SIS, but are an integral subset of others devices, i.e., typically a sensor of some nature (e.g., pressure sensor), a PLC or DCS (host device that receives the sensor input and outputs safety signal) and an operator, which may consist of a valve and an actuator. The components of an SIS should each have a SIL capable rating that enables a safety engineer to select the individual devices based upon their respective average Probability of Failure on Demand (PFD). PFD is the probability that a device will not function when called upon in an SIS. The average PFD for each device is added together to total the system PFD. This SIL total determines the overall SIL rating for the system in question. Selecting a low average PFD for each component in the SIS increases the risk tolerance of the safety system.

Electronic Actuators and SIL

Electronic actuators are classified as type B, complex devices or devices containing microprocessors, microcontrollers, and ASICs by IEC 61508. Some electronic actuator manufacturers supply separate hardware devices to bypass their internal microprocessors in order to acquire SIL certification. Flowserve Limitorque’s MXa is SIL certified without adding unique hardware modules, meaning that another potential point of failure is removed from the safety system. It is SIL 3 capable in “as built” configuration. In fact, when compared to other actuator providers, the MXa’s PFD (Probability of Failure on Demand) is the lowest in the industry for a type B, complex device. The PFD can be improved by regularly exercising the actuator. This can be accomplished by performing a partial stroke test (PST), which is standard configuration for the MXa. It is highly recommended that a monthly PST be performed to improve the average PFD of the MXa.

For the MXa, SIL 2 is identified as Basic ESD and PST (moves when commanded) in a “1oo1” configuration (one out of one means that only one actuator is required to ensure the SIL 2 requirement is achieved). SIL 3 identified as Stay-put (no unsolicited movement), Enhanced ESD and PST in a “1oo2” configuration (one out of two means that redundant actuators and valves are required to ensure the SIL 3 requirement is achieved).

<table>
<thead>
<tr>
<th>Mission Time</th>
<th>1 year</th>
<th>3 years</th>
<th>5 years</th>
</tr>
</thead>
<tbody>
<tr>
<td>10 yr</td>
<td>2.96 E-3</td>
<td>5.93 E-3</td>
<td>9.52 E-3</td>
</tr>
<tr>
<td>15 yr</td>
<td>3.22 E-3</td>
<td>6.51 E-3</td>
<td>9.77 E-3</td>
</tr>
<tr>
<td>20 yr</td>
<td>3.47 E-3</td>
<td>6.60 E-3</td>
<td>1.00 E-2</td>
</tr>
</tbody>
</table>

PFD table for MXa based upon monthly PST (Partial Stroke Test)
MX Multi-turn Smart Actuator

Heavy-duty Handwheel

Three-phase Motor

Declutch Lever

Cast Aluminum Housing

Absoluate Encoder

Worm Gear Set

Vehicle Terminal Compartment

Double-sealed Design

External Terminal Compartment

Plug-in Connectors

Control Chamber

Hall-effect Local Control Switches

LCD Multilingual Display

MXa — up to SIL 3 capable, even when option boards are installed!
MXa and SIL Certification

The SIL certification for the basic MXa, awarded by exida® Certification Services, now includes a suite of option boards which meet the requirements for systematic integrity up to SIL 3. A SIL 2 or SIL 3 capable MXa can include even network protocol field units, e.g., Foundation Fieldbus H1, Profinet & PA, DeviceNet HART and Modbus DDC. The MXa is identified as SIL 3 capable, meaning it is suitable for any safety integrity levels up to SIL 3, even with analog or digital out PCBs, or if installed into an arctic environment down to (-60°C). Please note that to meet the requirements of exida Certification for the SIL 2 or SIL 3 capable MXa the electronic actuator must be installed, configured and operated, and PST performed at regularly defined intervals. Please consult SIL Safety Manual, LMENIM2350, located on www.limitorque.com for complete instructions.

MXa and Failures in Time

For the user this means an MXa can be ordered with any combination of option boards, including network protocol field units, with the added confidence that each option has been analyzed by exida Certification. exida performed an FMEDA analysis for both the basic MXa and its associated option boards. Each option board was supplied an FIT (Failure in Time) calculation. A device failure is classified by IEC 61508 as a particular event which impacts proper and expected performance when requested. Failures In Time (FIT) is an indication of the number of failures in 10^9 hours, or approximately 114 000 years. So, an FIT of “1” would mean one failure can be expected every 114 000 years. The symbol used for the FIT calculation is Lambda (\(\lambda\)), and the subscript indicates the mode of failure, safe or dangerous, detected or undetected. The different classifications of “failures” and the expected response of an actuator are defined below:

- Fail-safe mode = Failure that causes the device to go to its defined fail-safe state without a demand from the process
  - ESD Mode = State in which the device is driven to its defined safe state (either open or close)
  - Stayput mode = State in which the actuator does not move (stays put)
- Safe detected = number of safe, detected failures in 10^9 hours. (\(\lambda_{SD}\))
- Safe undetected = number of safe, but undetected failures in 10^9 hours (\(\lambda_{SU}\))
- Fail dangerous, detected = Failure that is potentially dangerous and is diagnosed by device diagnostics in 10^9 hours. (\(\lambda_{DD}\))
- Fail dangerous, undetected = Failure that is potentially dangerous and is not diagnosed by device diagnostics in 10^9 hours (\(\lambda_{DU}\))

The FIT for the MXa option boards are:

<table>
<thead>
<tr>
<th>Device</th>
<th>(\lambda_{SD})</th>
<th>(\lambda_{SU})</th>
<th>(\lambda_{DD})</th>
<th>(\lambda_{DU})</th>
</tr>
</thead>
<tbody>
<tr>
<td>Arctic Option – ESD Mode</td>
<td>0</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>Backup Power Board – Stayput Mode</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>UPS Power Board – Stayput Mode</td>
<td>5</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>Analog Option Board – Stayput Mode</td>
<td>53</td>
<td>0</td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>HART Board – Stayput Mode</td>
<td>51</td>
<td>0</td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>HART Board – ESD Mode</td>
<td>0</td>
<td>0</td>
<td>60</td>
<td></td>
</tr>
<tr>
<td>DeviceNet Board – Stayput Mode</td>
<td>9</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>Foundation Fieldbus Board and Profibus PA – Stayput Mode</td>
<td>47</td>
<td>0</td>
<td>0</td>
<td>10</td>
</tr>
<tr>
<td>MODBus Board – Stayput Mode</td>
<td>7</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>Profibus DP Board – Stayput Mode</td>
<td>47</td>
<td>0</td>
<td>0</td>
<td>10</td>
</tr>
<tr>
<td>Profibus PA Board – ESD Mode</td>
<td>0</td>
<td>0</td>
<td>57</td>
<td></td>
</tr>
<tr>
<td>Relay Option Board – NI – Stayput Mode</td>
<td>11</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>Relay Option Board – Monitor – Stayput Mode</td>
<td>112</td>
<td>2</td>
<td>119</td>
<td>2</td>
</tr>
<tr>
<td>Arctic Option – Stayput Mode</td>
<td>0</td>
<td>9</td>
<td>0</td>
<td></td>
</tr>
</tbody>
</table>

MXa, Safe Failure Fraction (SFF), and Hardware Fault Tolerance (HFT)

The FIT for each option board can be used to develop a safety system’s safe failure fraction (SFF) equation. An SFF is expressed in percent of safe failures which correspond to the overall failure rate. A high SFF value lowers the probability of a dangerous event impacting the SIS, e.g., 75% SFF is better than 50%.

The SFF determines the range of acceptable hardware fault tolerance (HFT) for the safety instrumented system. An HFT is the device’s capability of acting on a safety signal in spite of system faults. The MXa and its suite of option boards has a Hardware Fault Tolerance of “0”, as determined by exida Certification. This means that, for a SIL 2 application, the MXa with any combination of option boards having an HFT of “0” can be installed into an SIS requiring an SFF range from 90% to < 99%. For an SIL 3 application, redundant MXa actuators with any combination of option boards can be installed into an SIS requiring an SFF of \(\geq 99\%\).
MXa SIL Advantages

Advantages of the SIL certification for the MXa include continued torque and position protection, even when a safety event occurs. Also, the need to use external devices to track the position of the actuator is removed—the MXa’s reliable Limigard feature ensures the user’s connection to the actuator’s internal relays do not need to be bypassed. No peripheral wiring is required to isolate the actuator from its internal programmable logic for safe operation. The internal safeguard features of the MXa are sufficient to report the actuator status and respond when an ESD is asserted.

The addition of the option boards and arctic temperature components indicates Flowserve Limitorque’s continued commitment to providing the safest, most reliable electronic actuator in the industry.

Glossary:

- **ESD** = Emergency Shut Down – configuration of an actuator so that it enters a “safe state” when plant control issues an emergency signal.
- **SIL** = Safety Integrity Level – relative level of risk reduction required for a Safety Instrumented Function (SIF). SIL is generally identified by levels of risk reduction, where SIL 1 is the least dependable classification to SIL 4, the most dependable classification.
- **SIF** = Safety Instrumented Function – the specific control functions performed by a Safety Instrumented System (SIS).
- **SIS** = Safety Instrumented System – generally, a system that is instrumented with hardware and software which are specifically used in critical process applications. They may consist of a process monitoring device that is connected to a programmable logic device that transmits to equipment that controls the safety and reliability of the process.
- **FMEDA** = Failure Modes Effects and Diagnostic Analysis – generally a procedure to determine in detail the causes of errors and their impact on a system.
- **PFD** = Probability of Failure on Demand – the probability that a device will not safely function when a dangerous failure occurs.
- **PST** = Partial Stroke Test – a test scenario which partially strokes the actuator/valve combination when enabled. Its purpose is to routinely actuate a valve in order to preclude or diagnose a potentially dangerous undetected event before it occurs.
- **FIT** = Failures in Time – generally defined as the frequency of failure for an engineered system or component, expressed in hours. For SIL evaluations, FIT is expressed in number of events in 10^9 hours.
- **SFF** = Safe Failure Fraction – expressed in percent of safe failures which correspond to the overall failure rate, e.g., SFF = (1 - (λ_{TD} + λ_{SD} + λ_{SU} + λ_{DU}))
- **HFT** = Hardware Fault Tolerance – the ability of the device to act upon a valid safety signal in spite of system faults. It is expressed in percentage.
- **RRF** = Risk Reduction Factor – the amount of reduction in risk gained by specifying an increase in SIL levels. An increase of one level (SIL 1 to SIL 2) reduces the RRF by a factor of ten (10).
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Flowserve Corporation
Flow Control

United States
Flowserve Limitorque
5114 Woodall Road
P.O. Box 11318
Lynchburg, VA 24506-1318
Phone: 434-528-4400
Facsimile: 434-845-9736

England
Flowserve Limitorque
Euro House
Abex Road
Newbury
Berkshire, RG14 5EY
United Kingdom
Phone: 44-1-635-46999
Facsimile: 44-1-635-36034

Japan
Limitorque – Nippon Gear Co., Ltd.
NOF Bldg. 9th Floor
1-11-11, Kita-Saiwai, Nishi-Ku
Yokohama (220-0004)
Japan
Phone: 81-45-326-2065
Facsimile: 81-45-320-5962

Singapore
Flowserve Limitorque
12, Tuas Avenue 20
Singapore 638824
Phone: 65-6868-4628
Facsimile: 65-6862-4940

China
Limitorque Beijing, Pte., Ltd.
RM A1/A2
22/F, East Area, Hanwei Plaza
No. 7 Guanghua Road, Chaoyang District
Beijing 100004, Peoples Republic of China
Phone: 86-10-5921-0606
Facsimile: 86-10-6561-2702

India
Flowserve Limitorque, Ltd.
Plot No 4
Export Promotional Industrial Park
Whitefield, Bangalore 560066
India
Phone: 91-80-40146200
Facsimile: 91-80-28410286

For more information on the features, options and certifications of the Limitorque MX, consult Flowserve bulletin LMENBR2302.

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